	Application No.	Applicant(s)
Notice of Allowability	09/848,086	NISHIZAWA, AKINORI
	Examiner	Art Unit
	Andrew H. Lam	2625
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>06/26/06</u> .		
2. The allowed claim(s) is/are 1, 2, 4-9 and 11-14, renumbered as claims 1-12.		
 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)		
1. Notice of References Cited (PTO-892)	5. Notice of Informal P	, ,
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	
Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date	7. 🛛 Examiner's Amendr	Paper No./Mail Date 7. ⊠ Examiner's Amendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material Output Description:	8. 🛭 Examiner's Stateme	ent of Reasons for Allowance
KAWilliano	9.	·
KIMBERLY WILLIAMS SUPERVISORY PAYENT EXAMINER		

DETAILED ACTION

 This action is responsive to the following communication: an Amendment filed on 06/21/06.

Response to Arguments

Applicant's arguments, see pages 5-8, filed on 06/26/06, with respect to claims 1, 2, 4-9 and 11-14 have been fully considered and are persuasive. The prior arts rejection of claims 11, 2, 4-9 and 11-14 has been withdrawn.

Allowable Subject Matter

Claims 1, 2, 4-9 and 11-14 are allowable over prior art of record. The following is an examiner's statement of reasons for allowance: Claims 1, 2, 4-9 and 11-14 of the current application are allowed for the reasons pointed out by the Applicant's remarks (pages 5-8).

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew H. Lam whose telephone number is (571) 272-8569. The examiner can normally be reached on M-F (9:30-6:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly Williams can be reached on (571) 272-7437. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for

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published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

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For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

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Business Center (EBC) at 866-217-9197 (toll-free).

KIMBERLY WILLIAMS
SUPERVISORY PATENT EXAMINER

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EXAMINER'S AMENDMENT

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Amend the following:

Claims

1. (Currently amended) The An image forming apparatus comprising: a plurality of processing circuits categorized into first and second blocks with respect to respective functions of the plurality of circuits; a power transformer having a plurality of secondary winding; a first power supply unit for always feeding DC current to the first block of processing circuits from at least one of the plurality of secondary windings; a second power supply unit for feeding DC current to the second block of processing circuits from the plurality of seconding secondary windings other than the at least one of the plurality of secondary windings; at least one switch located between the plurality of secondary windings and the second block of processing circuits for interruption of power supply to the second block of processing circuits; a control unit for controlling the respective switch such that the DC current from the secondary windings to the second block of processing circuits is interrupted in a power save mode and the DC current is fed to the second block of processing circuits from the secondary windings in a normal mode; and a voltage converting circuit for converting DC voltage of the first power supply unit to DC voltage of another level such that the DC voltage of another level is fed to the second block processing circuits in the power save mode.